

DRBD: Dynamic Reliability Block Diagram for System Reliability Modeling

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Acknowledgement

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Outline

- DRBD controller component blocks
- Development of DRBD models (example)
- Formal specifications of DRBD constructs
- Formal verification of DRBD models
- Conversion of DRBD models into colored Petri nets (CPN)
- Case study: modeling, verification
- Conclusions and future work

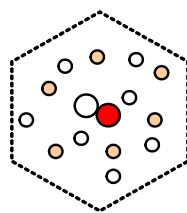


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A Motivating Example



- Primary Cluster Head
- Secondary Cluster Head
- Sensor Nodes in S1
- Sensor Nodes in S2

Initially, sensor nodes in S1 are operational; sensor nodes in S2 are in a sleeping mode


- When the primary cluster head fails, the secondary cluster head will be automatically activated.
- Sensor nodes in S1 can be put into a sleeping mode, and sensor nodes in S2 will be activated.
- How to model the state dependency between S1 and S2: Deactivation -> Activation dependency?

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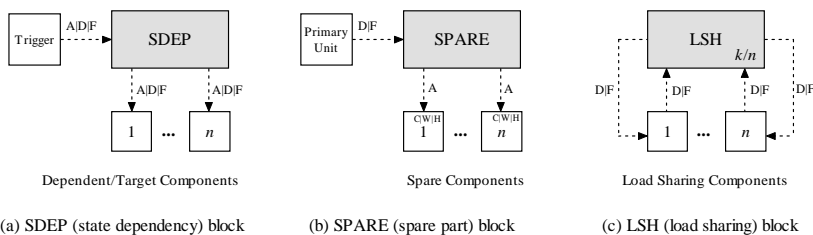
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The State of the Art

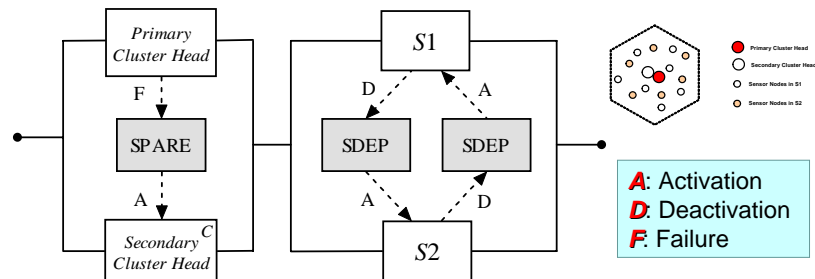
- Most of the existing reliability modeling tools (e.g., RBD) cannot capture the state dependency between components. 
- Other tools, such as Dynamic Fault Tree (DFT), may support modeling a functional dependency
 - The failure of a component causes some other dependent components to become inaccessible or unusable
 - However, it still cannot capture the Deactivation -> Activation state dependency between components.
- We propose a set of new **Dynamic Reliability Block Diagram (DRBD)** constructs as an extension to the existing RBD modeling tool.

DRBD Controller Component Blocks



- **A** stands for an activation event occurred on a component that leads to an *Active* state of that component,
- **D** stands for a deactivation event occurred on a component that leads to a *Standby* state of that component, and
- **F** stands for a failure event occurred on a component that leads to a *Failed* state of that component.

DRBD Model of the WSN Example



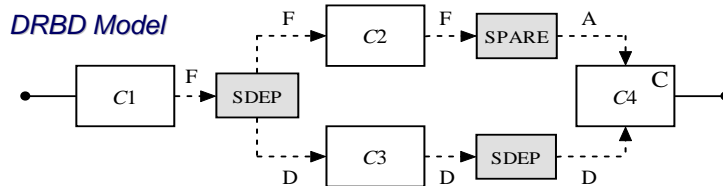
- The failure of the primary cluster head will automatically activate the secondary cluster head.
- The components labeled S1 and S2 represent the two sets of sensor nodes that may work alternatively.
- The deactivation of S1 (S2) will automatically activate S2 (S1).

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Formal Specifications



- To support formal verification and validation of our proposed DRBD model, it is necessary to formally define the DRBD modeling constructs.
 - Provide the denotational semantics for the development of DRBD models in a precise manner.
 - Help to eliminate ambiguity in a constructed DRBD model.
- **Question 1:** When component C1 fails, will C4 be in a state of *Active* or *Standby*, or will the result be nondeterministic?

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Object-Z Specification

Event ::= Activation | Deactivation | Failure

SDEP

```

trigger : Component
targets : P Component
nTargets : N
triggerEvent : Event
targetEvents : Component -> Event
sdep : T x Component x Event -> P(T x Component x Event)

nTargets = #targets ^ nTargets > 0 ^ targets = dom targetEvents
forall c in targets . c != trigger ^ probability(c | triggerEvent) != probability(c)
  ^ probability(triggerEvent | c) = probability(triggerEvent)
{(t, trigger, triggerEvent) | t in T} = dom sdep
    
```

Activate Trigger

```

Delta(trigger, targets)
t? : T
(triggerEvent = Active) (trigger.state' = Active)
forall c in targets . (t? + delta_c, c, targetEvents(c)) in sdep(t?, trigger, triggerEvent)
  ^ ((targetEvents(c) = Activation ^ c.state' = Active)
  v (targetEvents(c) = Deactivation ^ c.state' = Standby)
  v (targetEvents(c) = Failure ^ c.state' = Failed))
    
```

Deactivate Trigger

Fail Trigger


- The target events do not occur simultaneously, but with some random time delay δ_c for target component c .
- The failure of C_2 and deactivation of C_3 will not happen immediately after the failure of C_1 .
- Which state C_4 will be in (*Active* or *Standby*) is nondeterministic.
- **Question 2:** How can we be confident that the model is an accurate representation of the actual system?

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Formal Verification Approach

- Testing or simulations are not suitable for verifying DRBD models because it is almost impossible to cover all cases. 
- Use formal methods (e.g., model checking techniques) to verify the behavioral properties of a DRBD model before the evaluation process starts.
- Use temporal logic to specify system properties
 - **Property P:** "If component A fails, component B and C will also fail, which leads to the failure of the whole system S ."
 - The temporal formula in LTL (Linear Temporal Logic) can be written as $[] (\neg A \rightarrow (\neg B \wedge \neg C) \wedge \langle \rangle \neg S)$
- When a DRBD model is proved to be incorrect
 - Any quantitative evaluation results might be unusable.
 - The DRBD model needs to be fixed.

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Formal Verification Models

- DRBD models are not formally defined & executable.
- Object-Z specifications of DRBD constructs are formal specifications, however
 - Are not feasible for verification of behavioral properties.
 - Have no effective analysis and verification tool support.
- Convert a DRBD model into a formal executable model such as a state machine or a Petri net model.
- We adopt Colored Petri Net (CPN) model because
 - Is user friendly based on its graphical notations.
 - Has powerful, but intuitive rules for defining structure and dynamic behaviors.
 - Has many existing analysis and verification tools.



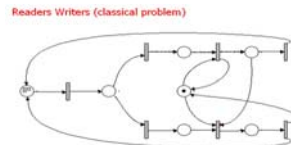
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Introduction to Petri Net

- “Three-in-one” capability of Petri net models [Murata 1989]
 - Graphical representation
 - Mathematical description
 - Simulation tool



- Definition:

A Petri net is a 4-tuple, $PN = (P, T, F, M_0)$ where

$P = \{P_1, P_2, \dots, P_m\}$ is a finite set of places;

$T = \{t_1, t_2, \dots, t_n\}$ is a finite set of transitions;

$F \subseteq (P \times T) \cup (T \times P)$ is a set of arcs (flow relation);

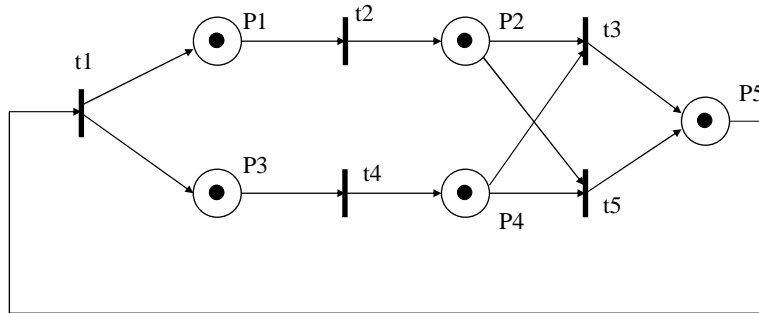
$M_0: P \rightarrow \{0, 1, 2, 3, \dots\}$ is the initial marking.

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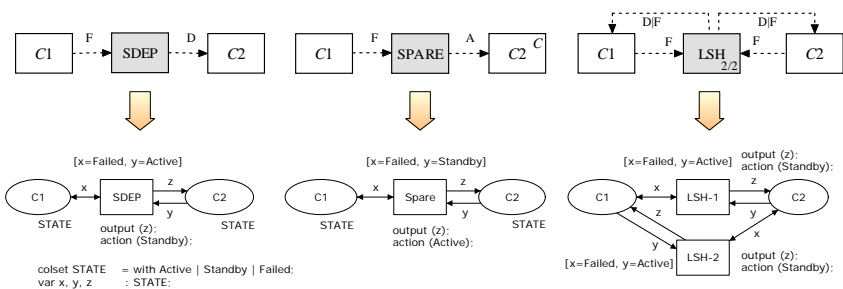
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An Ordinary Petri Net



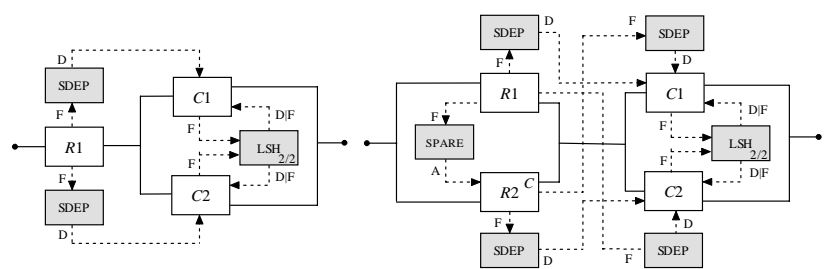
- In an ordinary Petri net, tokens are all of color **black**.
- In a Colored Petri net (CPN or CP-net),
 - Colors of tokens can represent values.
 - A transition may have a guard and executable code.

Convert DBBD into CPN Models



- Define three different colors/states: *Active*, *Standby* and *Failed*.
- A transition is associated with a guard and executable code
 - Can fire only if the guard $[x=Failed, y=Active]$ evaluates to *true*.
 - Code $output(z) ; action(Standby)$ deposits a *Standby* token in C2.

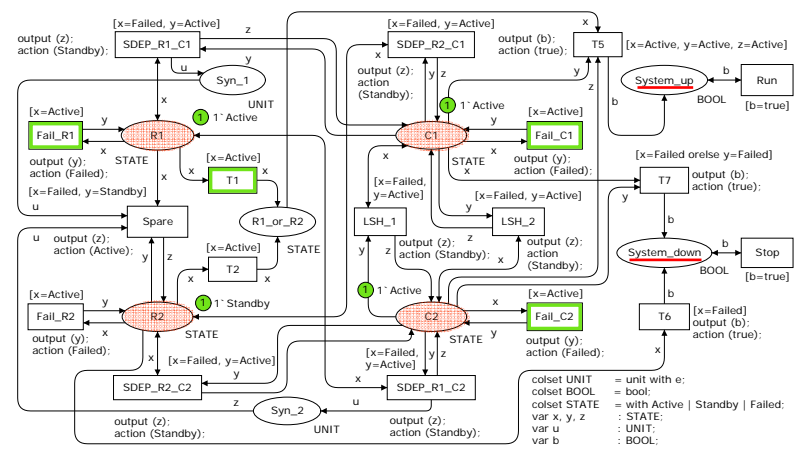
A Case Study



(a) Load sharing servers connected to a router (b) Load sharing servers connected to a router with a CSP

- Router R1 is connected to two server computers C1 and C2.
 - Server computers C1 and C2 are load sharing servers.
 - When router R1 fails, the computers C1 and C2 will be deactivated.
- To make the system more reliable, we introduce a cold spare (CSP) for router R1, which is represented by component R2.

Colored Petri Net Model



Analysis Results

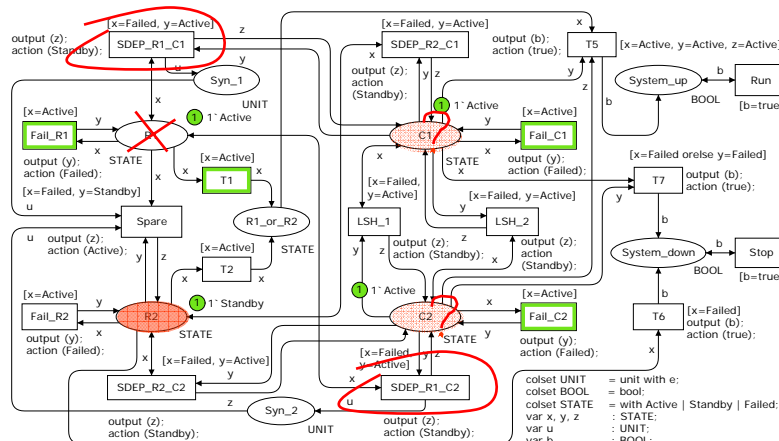
Result-1	Result-2
<pre> Statistics ----- State Space Nodes: 33 Arcs: 69 Secs: 0 Status: Full Scc Graph Nodes: 33 Arcs: 62 Secs: 0 Liveness Properties ----- Dead Markings [32] Dead Transition Instances Router'SDEP_R2_C1 1 Router'SDEP_R2_C2 1 Live Transition Instances None </pre>	<pre> DeadMarking(32) ----- val it = true : bool print(NodeDescriptor 32) ----- 32: C1 1: 1`Standby C2 1: 1`Standby R1 1: empty R2 1: empty R1_or_R2 1: 1`Active Syn_1 1: empty Syn_2 1: empty System_down 1: empty System_up 1: empty val it = () : unit Reachable'(1, 32) ----- A path from node 1 to 32: [1, 3, 11, 25, 30, 32] val it = true : bool </pre>

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Deadlock in CPN

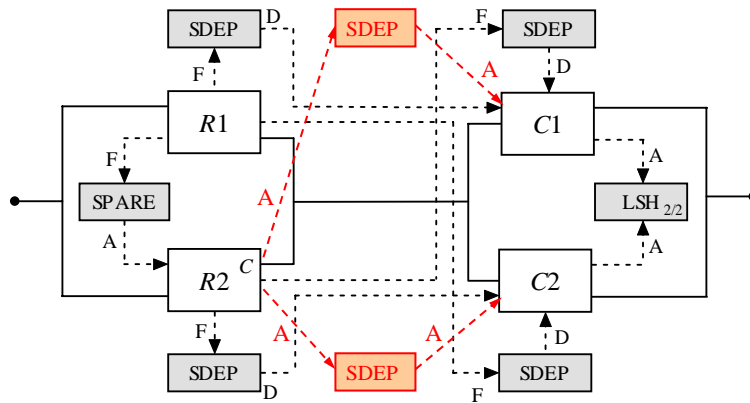


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Revised DRBD Model



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Analysis Results (after revision)

Result-3	
Statistics	

State Space	
Nodes:	67
Arcs:	162
Secs:	0
Status:	Full
Scc Graph	
Nodes:	67
Arcs:	141
Secs:	0
Liveness Properties	

Dead Markings	None
Dead Transition Instances	None
Live Transition Instances	None

- Fix the colored Petri net model by adding
 - New transition *SDEP_R2_C12*
 - New synchronization place *Syn_3*
 - And arcs and guards
- The analysis results show no deadlock markings.
- **Question 3:** How to verify additional properties?



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Model Checking Results

Formulas	ASK-CTL in ML	After Rev	Before Rev
Formula_1	<pre>val myASKCTLformula = EXIST_UNTIL(TT,NOT(MODAL(TT))); eval_node myASKCTLformula InitNode;</pre>	false	true
Functions	<pre>fun R1_Failed n = (Mark.R1 1 n = 1`Failed); fun R2_Failed n = (Mark.R2 1 n = 1`Failed); fun SystemFailed n = (Mark.System_down 1 n = 1`true);</pre>	-	-
Formula_2	<pre>val isFailed = FORALL_UNTIL(TT, NF("",SystemFailed)); val system = OR(NOT(NF("", R2_Failed)), isFailed); val myASKCTLformula = INV(system); eval_node myASKCTLformula InitNode</pre>	true	true
Formula_3	<pre>val isFailed = FORALL_UNTIL(TT, NF("",SystemFailed)); val system = OR(NOT(NF("", R1_Failed)), isFailed); val myASKCTLformula = INV(system); eval_node myASKCTLformula InitNode;</pre>	false	true

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Conclusions and Future Work

- Proposed a new modeling approach called Dynamic Reliability Block Diagrams (DRBD)
 - Resolves the shortcomings of the existing work.
 - Provides a powerful but easy-to-use reliability modeling tool for complex and large computer-based systems.
 - Supports automated verification of DRBD models.
- Develop a software tool that can automatically translate DRBD models into colored Petri nets.
- Study efficient evaluation methods for DRBD models.
- Develop a comprehensive system reliability modeling tool that supports editing, formal verification, and evaluation of DRBD models.

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Related Publications

- **R. Robidoux, H. Xu, and L. Xing** Towards Automated Verification of Dynamic Reliability Block Diagrams. *To be submitted to journal*, Computer and Information Science Dept., UMass Dartmouth, November 2007.
- **L. Xing, H. Xu, S. V. Amari, and W. Wang** A New Framework for Complex System Reliability Analysis: Modeling, Verification, and Evaluation. *Submitted to Journal of Autonomic and Trusted Computing (JoATC)*, September 2007.
- **H. Xu, L. Xing, and R. Robidoux** DRBD: Dynamic Reliability Block Diagrams for System Reliability Modeling. *Submitted to International Journal of Computers and Applications (IJCA)*, August 2007.
- **H. Xu and L. Xing** Formal Semantics and Verification of Dynamic Reliability Block Diagrams for System Reliability Modeling. In *Proceedings of the 11th International Conference on Software Engineering and Applications (SEA 2007)*, November 19-21, 2007, Cambridge, Massachusetts, USA.

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Questions?

The slides for this talk can be downloaded from

<http://www.cis.umassd.edu/~hxu>